

A Low-Power Dual-Processor Computing System for Advanced Nanosatellite Missions

Nathaniel Colson
Purdue University
ncolson@purdue.edu

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Overview

Overview

Attitude D & C

ZMobile DSP

Host Computer

Operation

Results

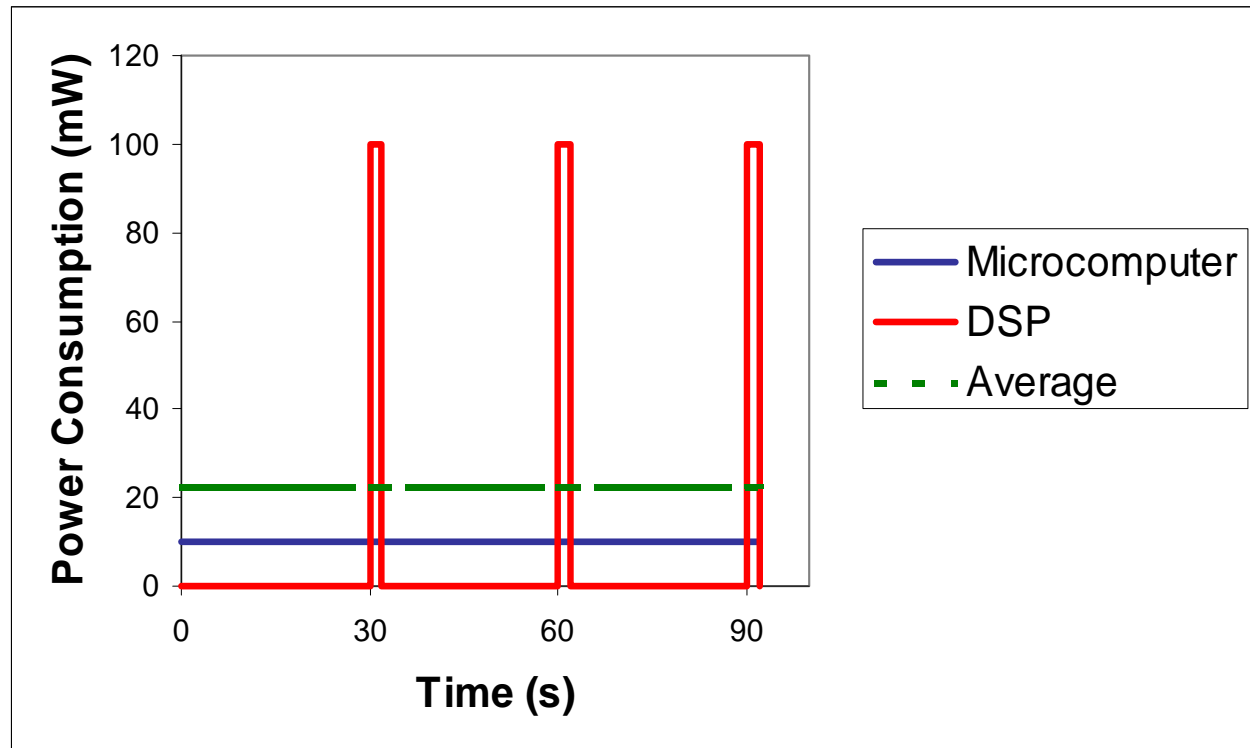
Conclusion

Two Processors:

- DSP
 - 500 MHz
 - 1 W
- Microcomputer
 - 8 MHz
 - <10 mW

Overview

- Very large computational power available
- Low average power consumption



Attitude Determination and Control

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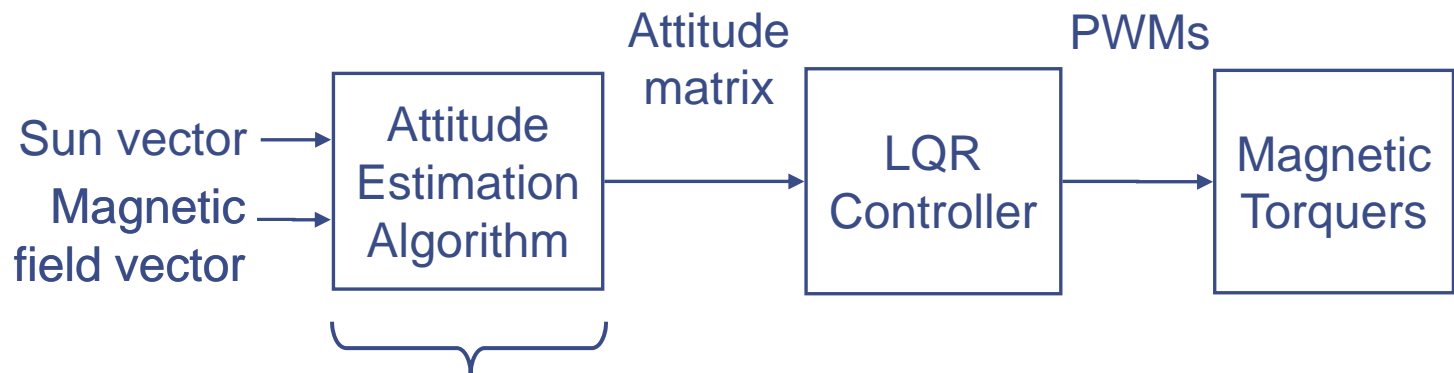
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- Primary engineering mission:
 - Attitude determination within 1 degree
 - Attitude control within 5 degrees

- Algorithm:



- Matrix / vector multiplications
- Matrix inversions
- Floating point variables

ZMobile DSP System

- Zmobile Mixed Signal board from Schmid Engineering:



- Graphical DSP programming with Embedded LabVIEW
- Four 14-bit A/D channels
- Five UARTS
- Programmable self-shutdown

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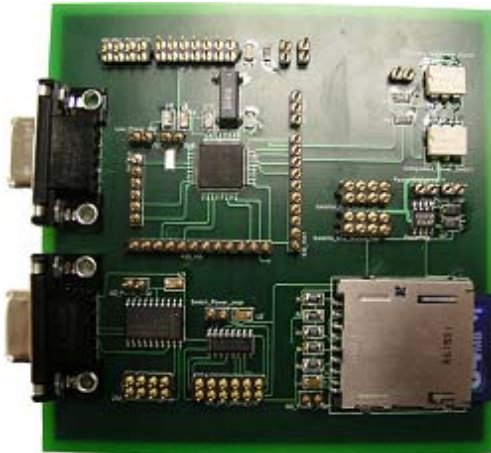
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Host Computer

- Tasks:
 - Generating wakeup signals for ZMobile DSP
 - Generating PWMs to drive torque coils
 - Performing all Command and Data Handling
- Communications On Board system



- Generic C&DH system from Taylor University
- Supports I2C, SPI, RS232
- Based on ultra low-power MSP430

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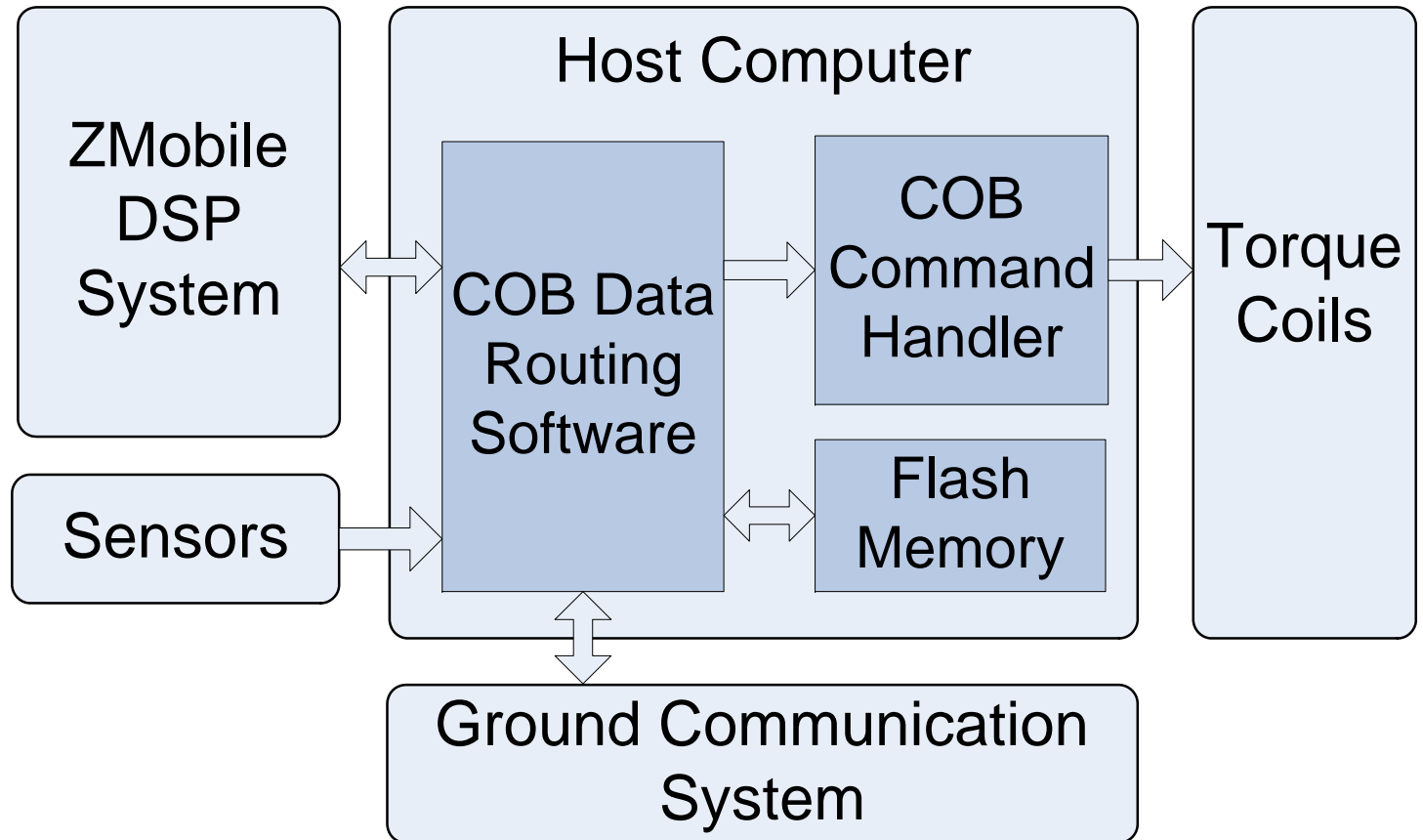
Host Computer

Microcomputer Upgrade:

- **MSP430-1611**
 - 10 KB RAM
 - 48 KB Flash Memory
 - 2.5 mA Supply Current
 - 2 Serial Interfaces
 - **MSP430-5438**
 - 16 KB RAM
 - 256 KB Flash Memory
 - 1.55 mA Supply Current
 - 4 Serial Interfaces
-
- 6-layer PCB
 - 4" x 4" board size
-
- 4-layer PCB
 - 4" x 2.4" board size

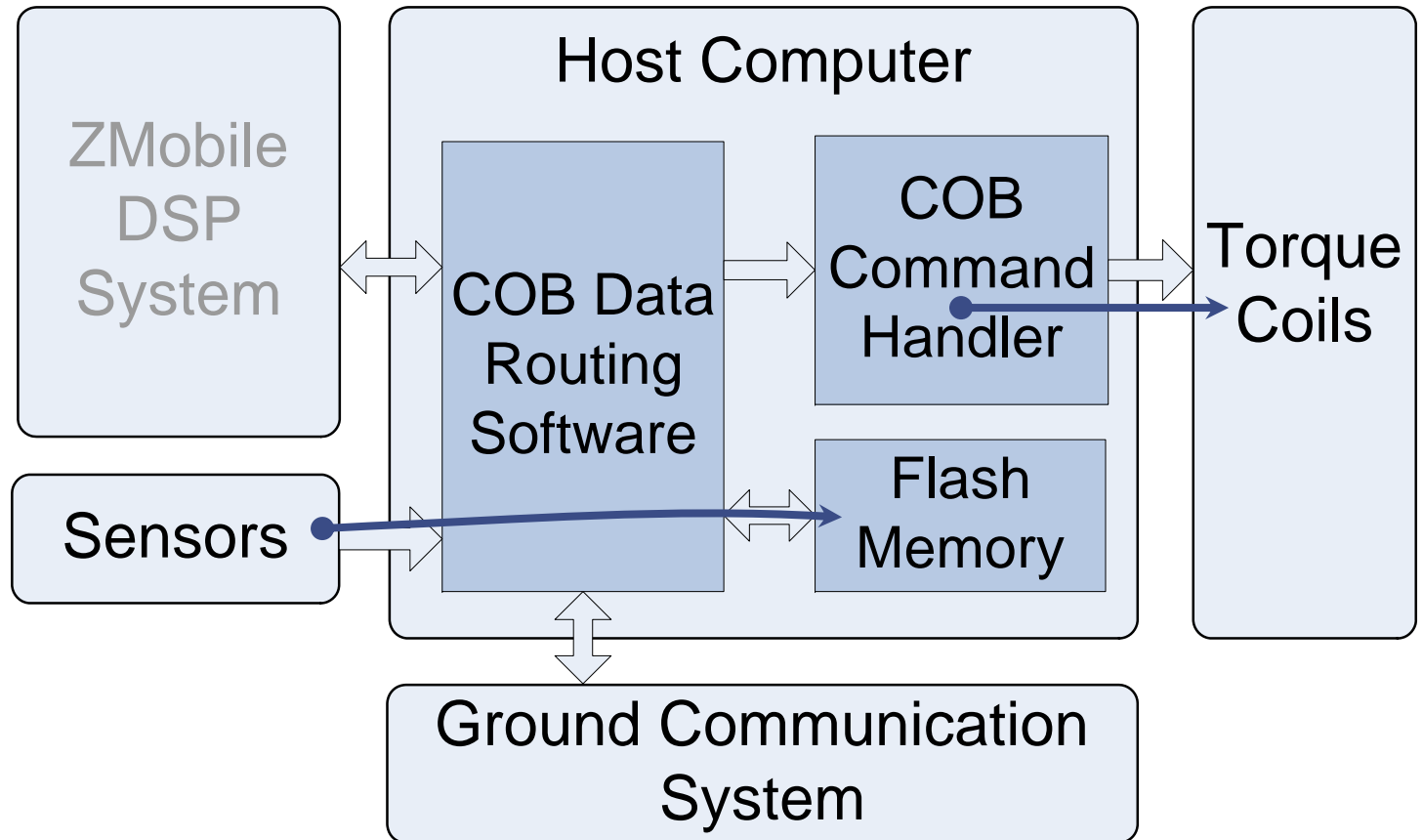
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System Operation

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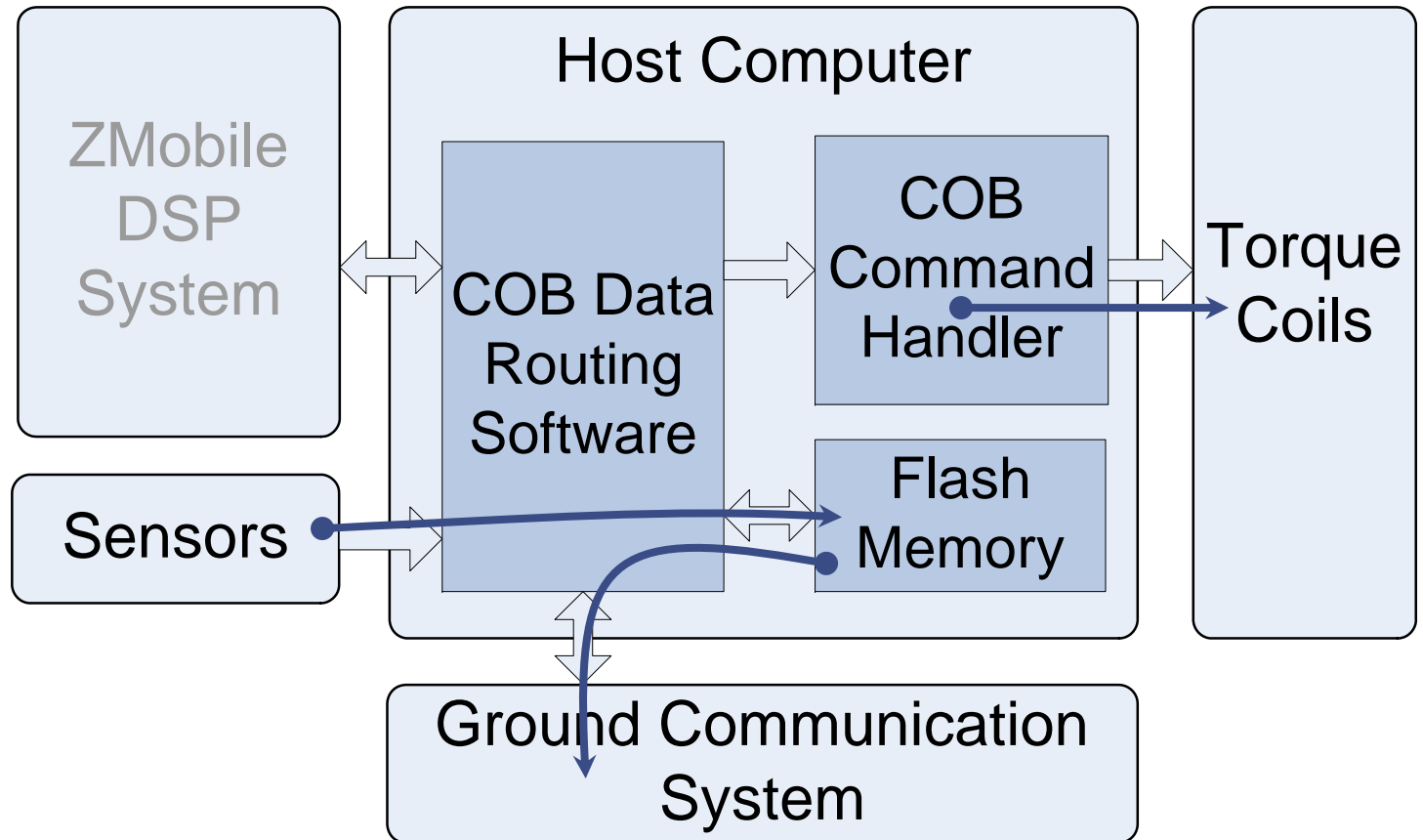
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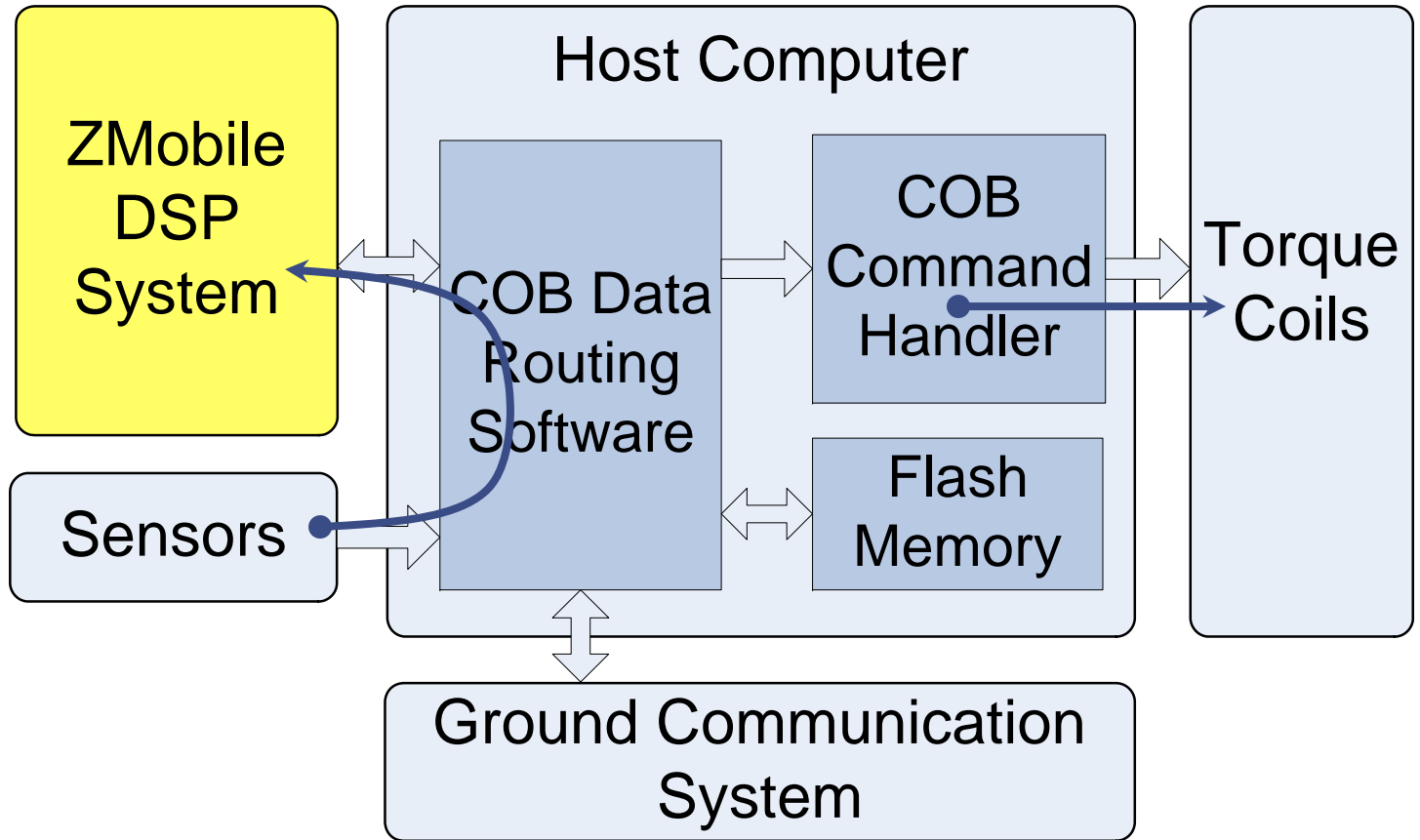
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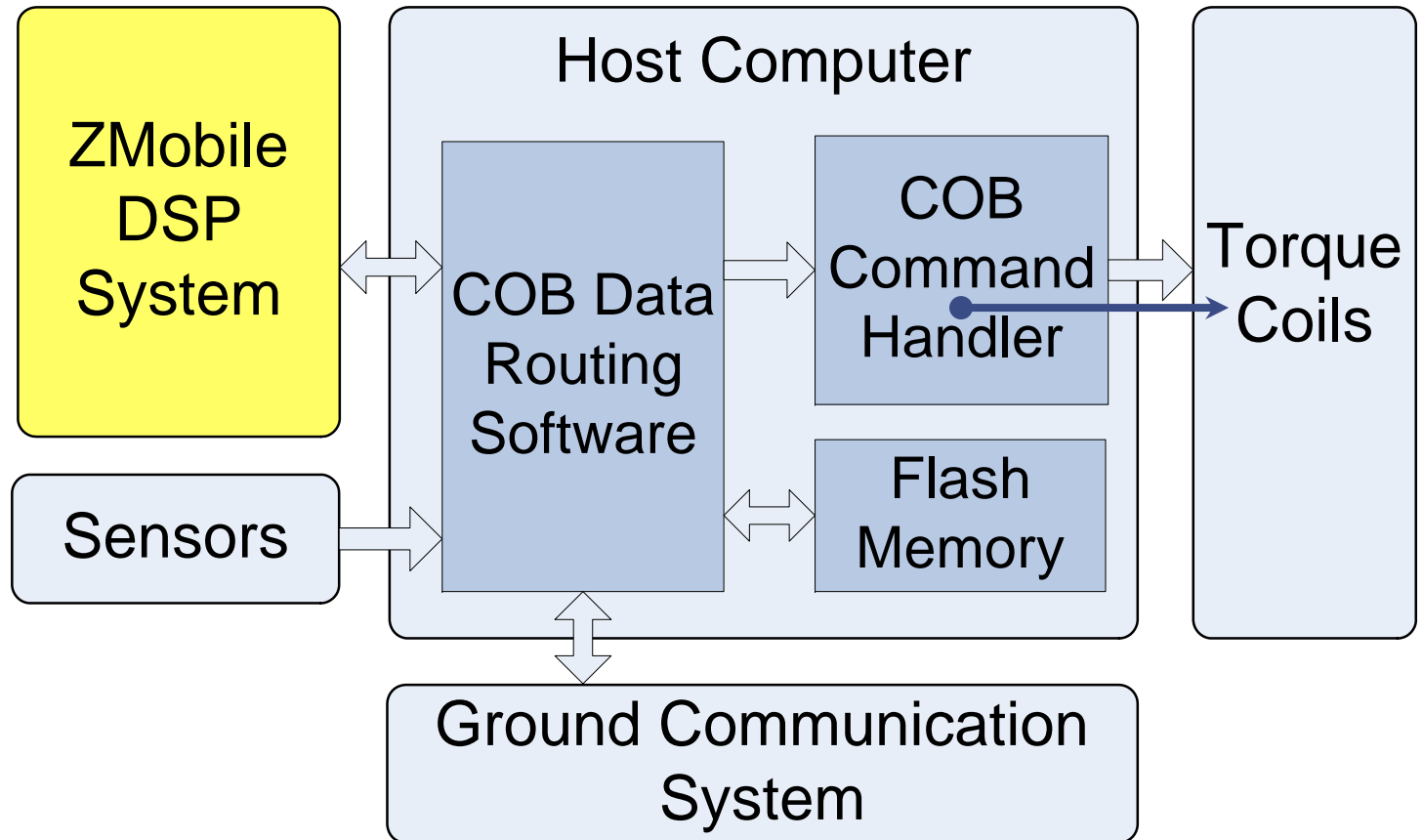
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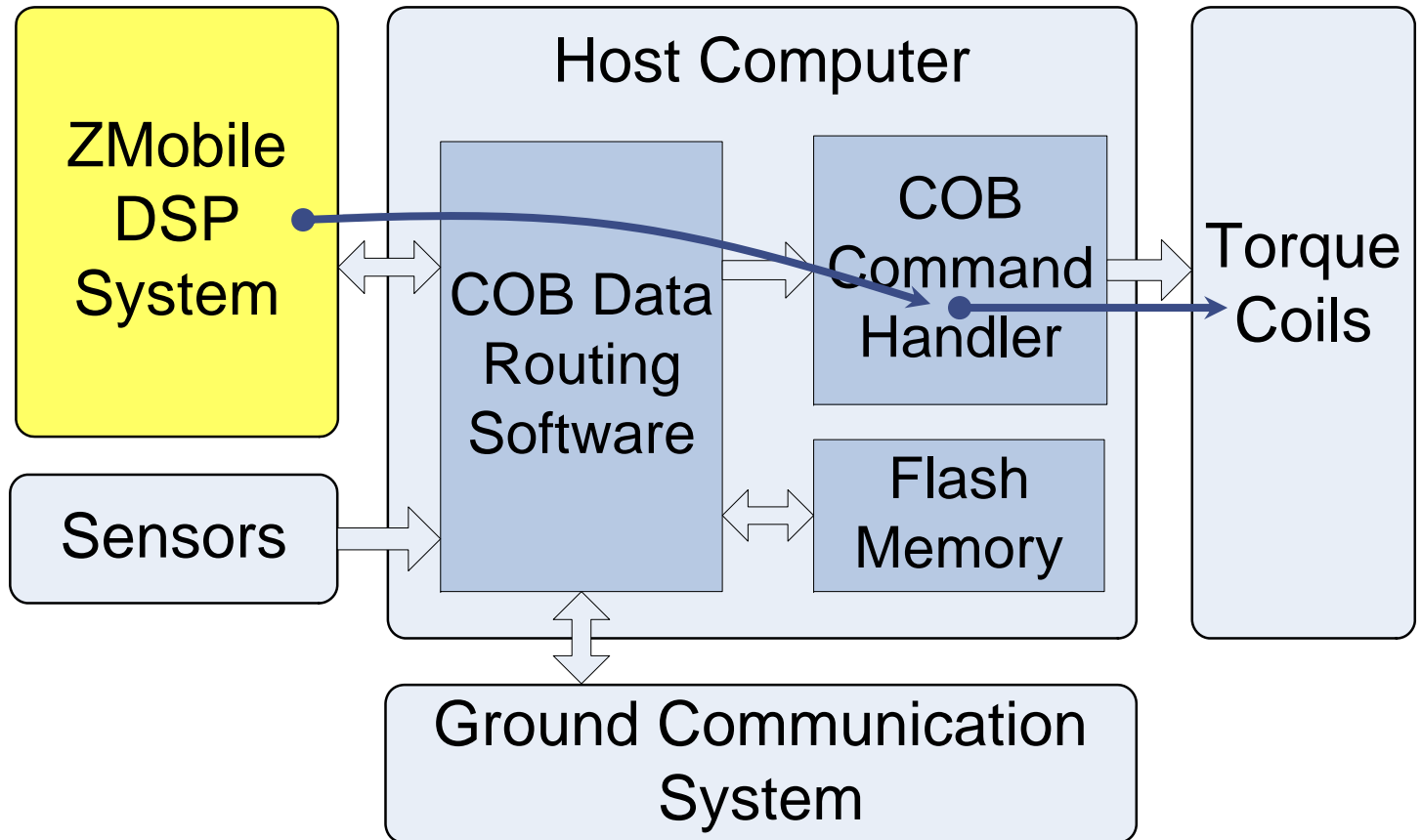
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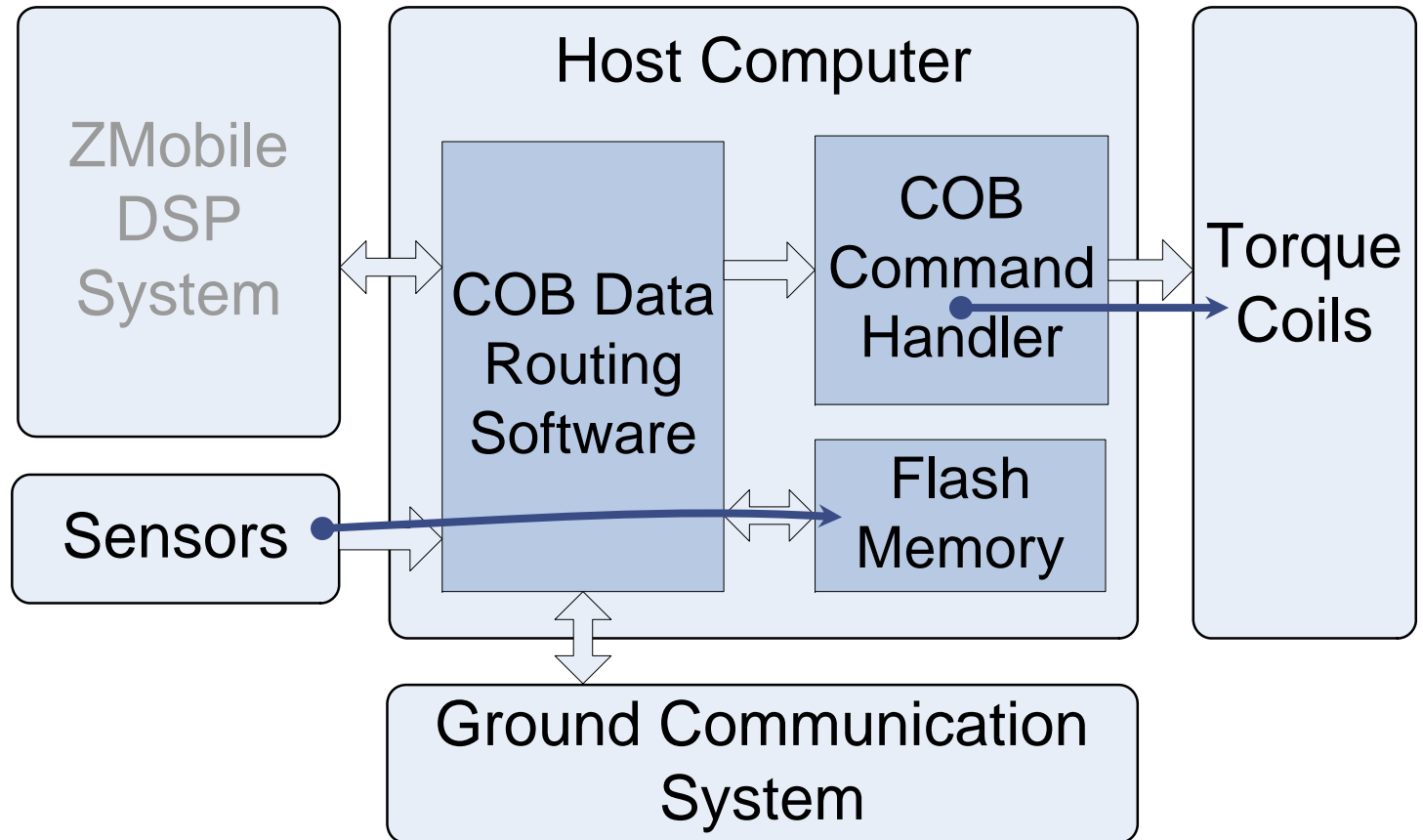
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Average Power Consumption

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$$\begin{aligned} & 1.0 \text{ W} * 10\% \text{ Duty cycle} \\ & \quad + \\ & 10 \text{ mW} * 100\% \text{ Duty cycle} \\ & \quad = \\ & < \mathbf{125 \text{ mW}} \end{aligned}$$

Design Verification

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Computational Performance



Power Consumption



Inter-processor Communication

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Nathaniel Colson

ncolson@purdue.edu

Paul Moonjelly

pmoonjel@purdue.edu

Faculty advisor: Prof. David Filmer

filmer@purdue.edu

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